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# GreenBus

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**14th ESCUG Meeting**

**FDL'06 Darmstadt**

# GreenSocs

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THE Open Source community based  
SystemC infrastructure project.

- Open to all to contribute / join / USE
- It is maintained and funded by subscriptions and contracts with GreenSocs Ltd.
- GreenSocs Ltd's "product" is  
co-ordination and delivery of open source  
infrastructure and construction of the open source  
community.
- GreenSocs uses a SourceForge repository to  
guarantee longevity.

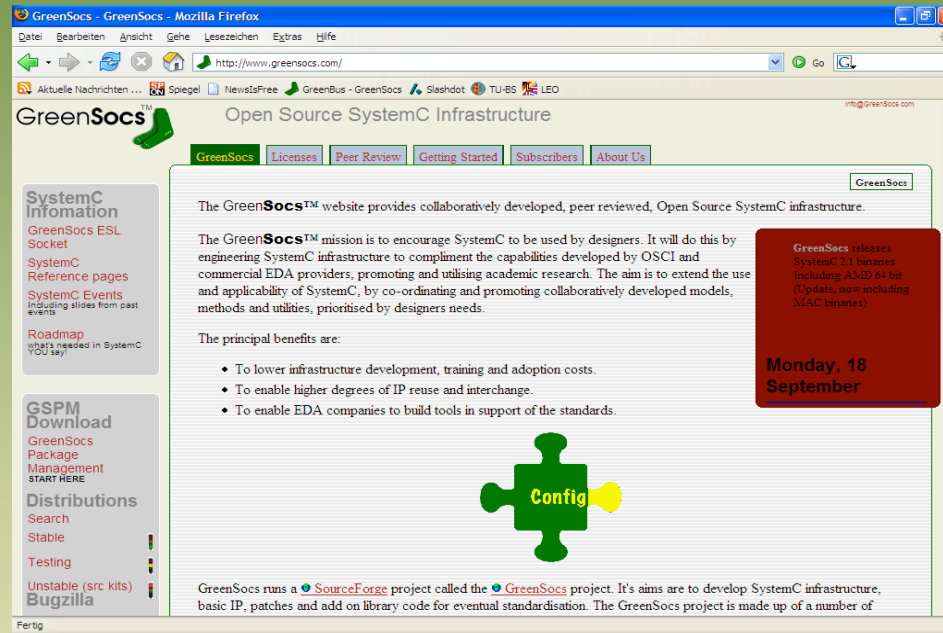
# GreenSocs SystemC Sandpit

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**NEEDS GUIDANCE:  
Subscribers and GreenSocs Engineering**

# Central User Resource



- **PUBLIC** Read/Write “information” Wiki
- GreenSocs projects pages (GreenBus)
- Other project pages
- **OPEN** - to encourage community...

# All Aboard



**NOKIA**  
Connecting People

*simplex sigillum veri*  
**VaST**™

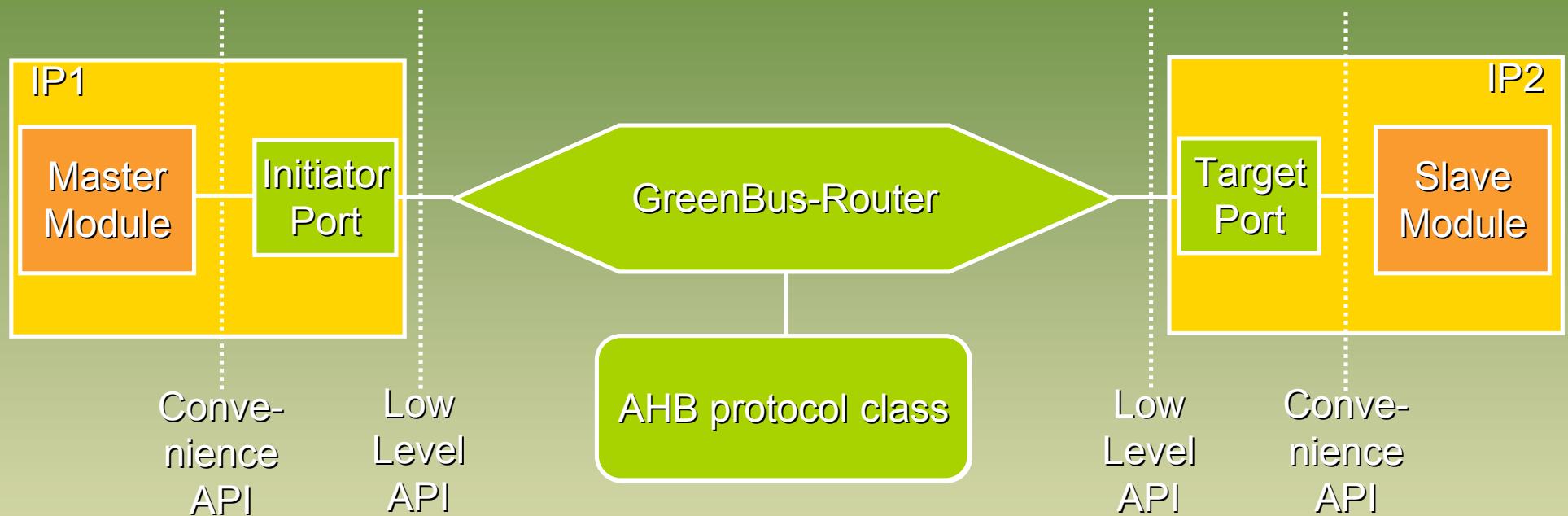


**TEXAS INSTRUMENTS**



**PHILIPS**

# GreenBus (cont'd)



- **Interoperability Layer: Generic Protocol**
- **User Layer: IP-specific convenience APIs**

# GreenBus

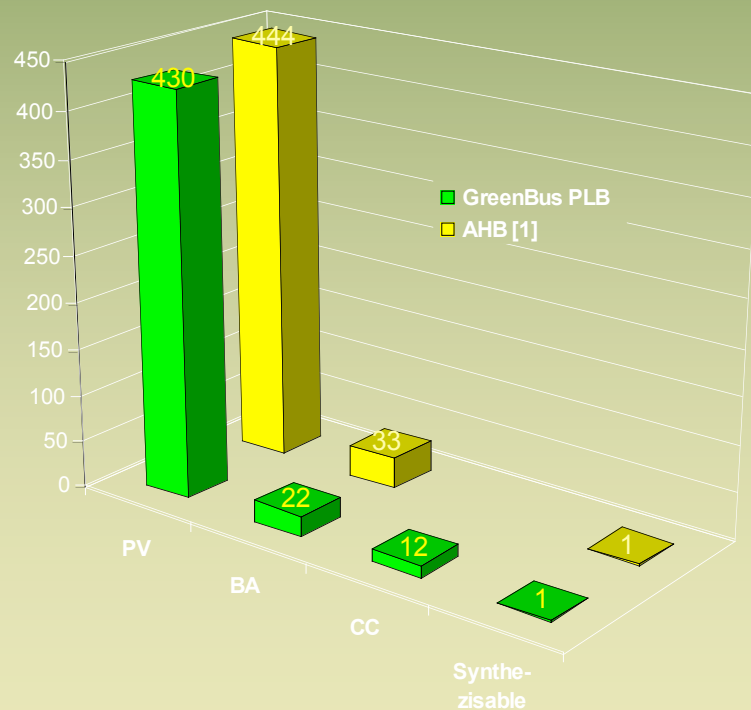
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- **GreenBus is not a bus!**
- **Inter-operability**
  - Simple API between models
  - Defined data structures
- **Safety**
  - Automatic memory management for data structures
  - Event semantics between models
- **Speed**
  - Data passed by shared pointers (payload-events)
  - Only required events used
  - Model free to use methods (rather than wait() calls)

**AVAILABLE**

# GreenBus (cont'd)

- Other features of GreenBus:
  - Open Source
  - Built for/on proposed TLM 2.0 standard



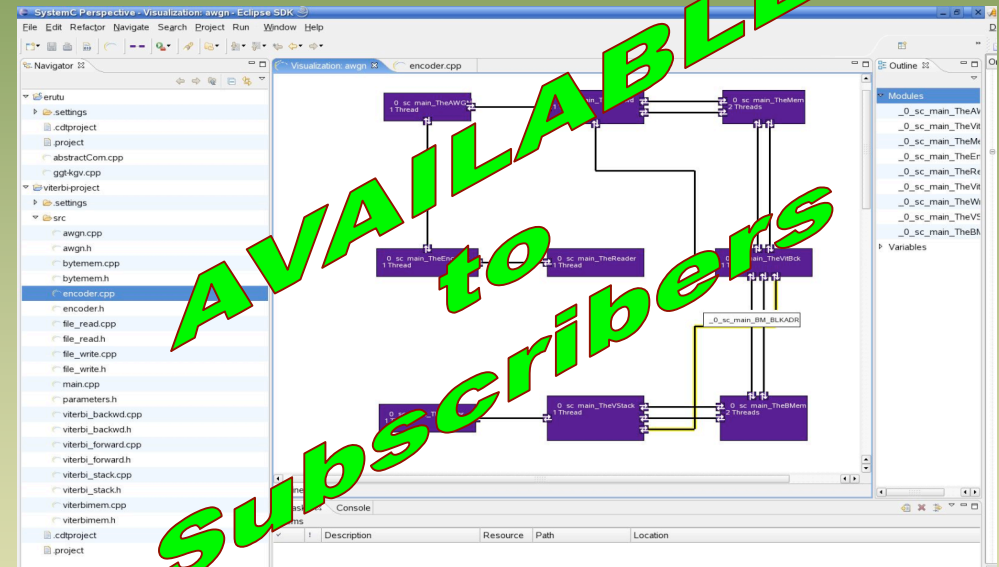
- Efficient (0.5 million atoms per second)
- Multiple abstraction levels
- Native support for other user API's (OCP-IP etc)

[DAC'06]



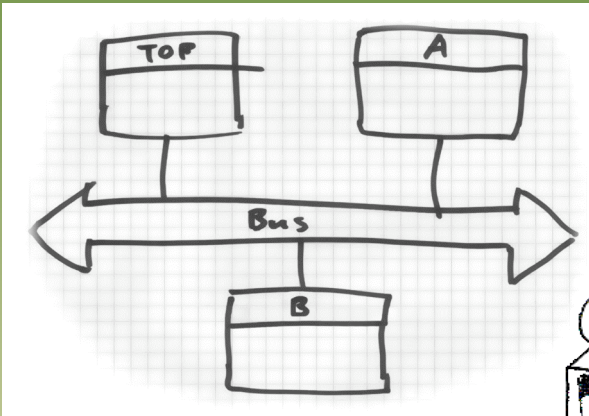
# Eclipse Environment

- Modular and extensible IDE for SystemC designs
- SystemC-specific features and extensions
  - Project management
  - Configuration
  - Visualization
  - Model execution
  - Module tree viewer
  - Structural representation
- Availability
  - Available for GreenSocs subscribers
  - Further customer-specific licenses and extensions are possible
  - GreenSocs funding will ensure public availability



# DUST

## Structure and Communication Analysis



Your SoC idea...

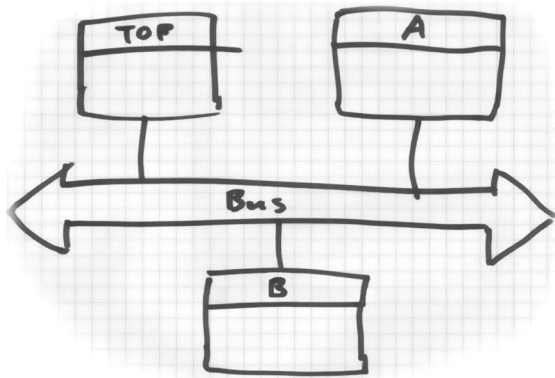


...your SystemC model...

```
class MORPH_DILATE : public sc_module {
public:
    // In
    sc_dus
    sc_dus_port<opb_slave_if, 0> slave_port;
    // Out
    sc_dus
    // Input ports
    class MORPH_ERODE : public sc_module {
    public:
        // Input port
        sc_dus
        // Output
        class dM_TOT_YUV : public sc_module {
        public:
            // SystemC ports
            // Re
            // GHIP output port that provides a stream of camera snapshots
            sc_dus_port<ship_master_if, YUVFrame> yuvframe_out;
            // Co
            class OPB_BUS : public sc_module, public opb_blocking_if {
            public:
                // Slave multi-port
                sc_dus_port<opb_slave_if, 0> slave_port;
                // Register this class with the SystemC simulator
                SC_HAS_PROCESS(OPB_BUS);
                // Create an OPB_BUS simulation model instance with 100MHz default clock
                OPB_BUS(sc_module_name name)
                {
                    sc_module(name);
                }
                init();
                OPB_TRACE("Created OPB model running at %d MHz clock frequency.\n", mhz);
                SC_THREAD(arbiter);
            }
            // Create an OPB_BUS simulation model with custom clock frequency
            // *param name SystemC module name
            // *param clock Clock frequency in MHz
            OPB_BUS(sc_module_name name, unsigned int clock)
            {
                sc_module(name);
            }
            init();
            mhz = clock;
            OPB_TRACE("Created OPB model running at %d MHz clock frequency.\n", mhz);
            SC_THREAD(arbiter);
        }
    };
};
```

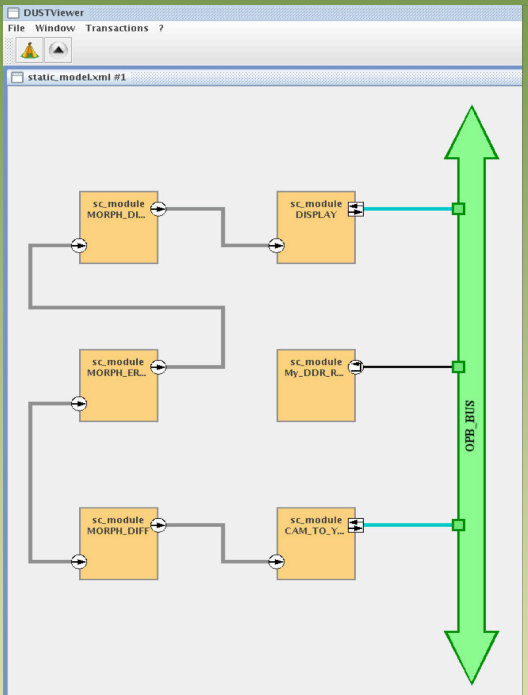
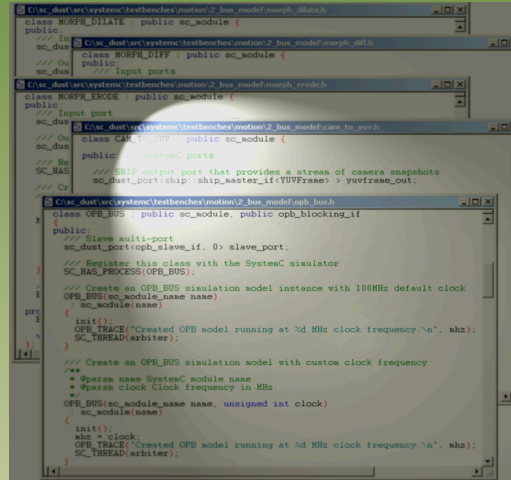
- Live design structure analysis
- Interactive transaction recording
- Component / Bus / MSC / Gantt View
- Works with any LRM-2.1+ kernel
- GreenBus support

# DUST



## Your SoC idea...

## ...your SystemC model...



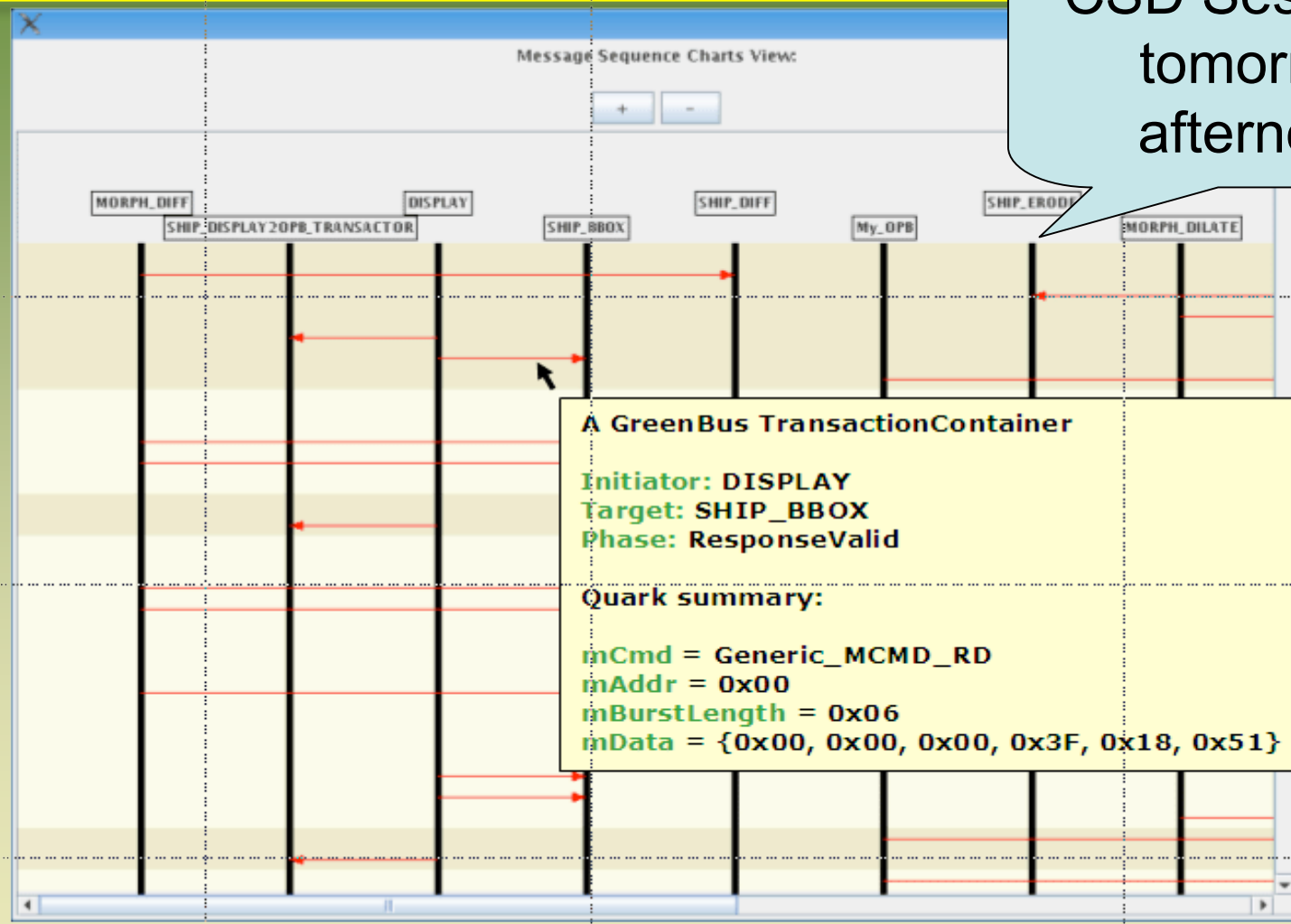
## ...the visual analysis

- **Live design structure analysis**
- **Interactive transaction recording**
- **Component / Bus / MSC / Gantt View**
- **Works with any LRM-2.1+ kernel**
- **GreenBus support**

# DUST

## Structure and Communication Analysis

CSD Session 3  
tomorrow  
afternoon



# Package Management

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## *EASE OF USE*

- Provide a common environment for packages
- Make it easy to  
    Install / Evaluate / Remove / Upgrade
- Keep track of dependencies, etc....
- Allow multiple installed “worlds”
- All packages can be qualified in a known environment (gcc, systemc, ...).
- Based on open source applications
- Available to ALL ESL vendors (can be used with flex etc)



# Package Management

## *EASE OF USE*

- **Provide a common environment for packages**
- **Make**  
**Ins**

```
> tar -zxf gsroot_2006.06-1_i386.tar.gz  
> ./gsroot/bin/gconfig  
> ./gsroot/bin/gsh
```
- **Keep**

```
$ gs-apt-get update
```
- **Allow**

```
$ gs-apt-get install gstlm
```
- **All pa**

```
$ cd examples/exaples_simple
```
- **enviro**

```
$ gscsim example_simple.cpp
```
- **Based on open source applications**
- **Available to ALL ESL vendors (can be used with flex etc)**



# Package Management

## *EASE OF USE*

- Provide a common e
  - Make it easy to Install / Eval
  - Keep track of d
  - Allow multiple
  - All packages can be installed in the same environment (gcc, etc)
  - Based on open source
  - Available to ALL E (flex etc)
- Lots of packages in the queue waiting to be done....
  - ST TAC
  - CoWare SCML
  - Mentor's O/S package
  - Etc...



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**That's it**

**Thank you!**

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**www.greensocs.com**